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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ravi P. Annavajhala
Serial No. : 10/626,469
Filed : July 24, 2003
Title : PROTECTION CIRCUIT

Art Unit : 2819
Examiner : Unknown

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits for the Examiner's consideration references listed on the attached form PTO-1449. This statement is being filed before the receipt of a first Office action on the merits. Also, since this application was filed after July 1, 2003, only copies of non-U.S. patent citations are being provided.

Applicant encloses herewith schematic entitled "HVP21LC.1" and dated January 24, 1997. The input VPPPAD is connected to a pad supply voltage. MH0, MH1, MH2 and MH3 are O devices (i.e. thick oxide devices described in the specification). MH4 is an M device (i.e. another type of thick oxide device described in the specification). HHVP12 is a node that is used internally on the integrated circuit as a high-voltage supply for programming and erasing EEPROM cells. The Examiner is invited to contact the undersigned in the event further information is desired concerning the foregoing schematic.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

August 19, 2004
Date of Deposit

Caryl Harriman
Signature

Caryl Harriman
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Page : 2 of 2

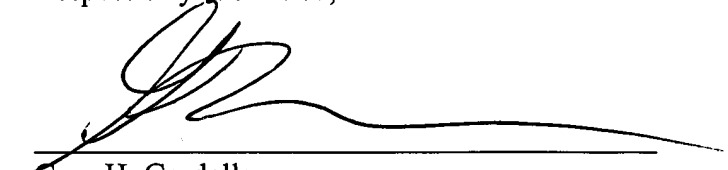
Attorney's Docket No.: 10559-523002 / P12427C

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

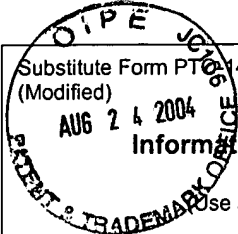
Date: _____

8/19/04



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|  Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b)) | U.S. Department of Commerce Patent and Trademark Office | | Attorney's Docket No. 10559-523002 | Application No. 10/626,469 |
| | Applicant Ravi P. Annavajjhala | | | |
| | Filing Date July 24, 2003 | | Group Art Unit 2819 | |

U.S. Patent Documents

| Examiner Initial | Desig. ID | Document Number | Publication Date | Patentee | Class | Subclass | Filing Date If Appropriate |
|------------------|-----------|-----------------|------------------|-------------------|-------|----------|----------------------------|
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| | AX | 6,362,942 | 3/26/2002 | Drapkin et al. | | | |
| | AY | 6,680,512 | 1/20/2004 | Nishikawa et al. | | | |

Other Documents (include Author, Title, Date, and Place of Publication)

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| Examiner Signature | Date Considered |
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | |

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| Substitute Form PTO-1449 (Modified) | U.S. Department of Commerce Patent and Trademark Office | Attorney's Docket No. 10559-523002 | Application No. 10/626,469 |
| Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b)) | | Applicant Ravi P. Annavajjhala | |
| | | Filing Date July 24, 2003 | Group Art Unit 2819 |

| Examiner Initial | Desig. ID | Document |
|---------------------|--------------|---|
| | AZ | Schematic "HVP21LC.1," (Jan. 24, 1997), corresponding to a circuit believed to have been incorporated in a product publicly distributed prior to January 2, 2002 (1 page) |

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|--|-----------------|
| Examiner Signature | Date Considered |
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | |